

Transmitted herewith for filing is the Patent Application of:

1 Inventor: KUAN-LUN CHANG, RUEY-HSIN LIU, TSYR-SHYANG LIOU, CHIH-MIN CHIANG, JUN-LIN TSAI

For: A METHOD OF FORMING A SHALLOW TRENCH-DEEP TRENCH ISOLATION REGION FOR A BICMOS/CMOS

TECHNOLOGY

Enclosed are:

6 sheets of drawing(s) - formal.
 An assignment of the invention to
 An associate power of attorney Applicant claims small entity status
 Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

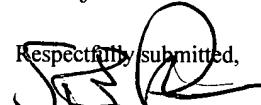
	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 770.
TOTAL CLAIMS	35 -20=	15	x 18 =	\$ 270.
INDEP CLAIMS	3 -3=	0	x 86 =	\$ 0.
			SUB TOTAL	\$ 1,040.
			ASSIGNMENT	\$0.
			TOTAL	\$ 1,040.

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Any additional filing fees required under 37 CFR §1.16.

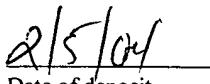
Any patent application processing fees under 37 CFR §1.17.

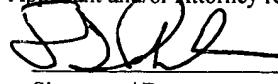
Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761

EXPRESS MAIL CERTIFICATE

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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.


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